# UNISONIC TECHNOLOGIES CO., LTD

# **DTC114Y**

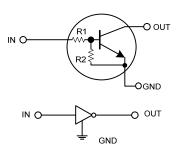
## **NPN SILICON TRANSISTOR**

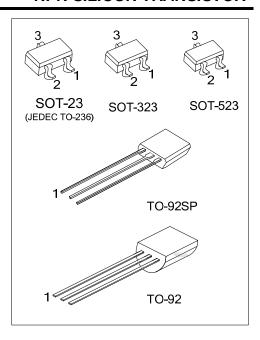
# NPN DIGITAL TRANSISTOR (BUILT- IN BIAS RESISTORS)

#### **FEATURES**

- \* Built-in bias resistors that implies easy ON/OFF applications.
- \* The bias resistors are thin-film resistors with complete isolation to allow negative input.

#### **EQUIVALENT CIRCUIT**

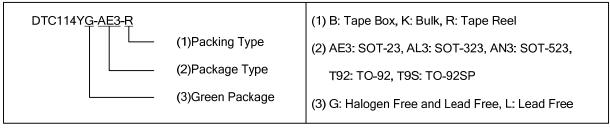




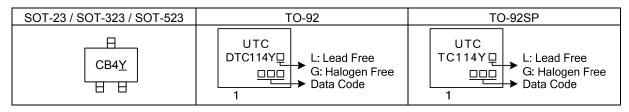
#### ORDERING INFORMATION

Ordering Number		Dealtons	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	DTC114YG-AE3-R	SOT-23	G	I	0	Tape Reel	
-	DTC114YG-AL3-R	SOT-323	G	-	0	Tape Reel	
-	DTC114YG-AN3-R	SOT-523	G	- 1	0	Tape Reel	
DTC114YL-T92-K	DTC114YG-T92-K	TO-92	G	0		Bulk	
DTC114YL-T92-B	DTC114YG-T92-B	TO-92	G	0		Tape Box	
DTC114YL-T9S-K	DTC114YG-T9S-K	TO-92SP	G	0	Ī	Bulk	
DTC114YL-T9S-B	DTC114YG-T9S-B	TO-92SP	G	0	Ī	Tape Box	

Note: Pin Assignment: G: GND O: OUT I: IN



#### **MARKING**



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### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless others specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Supply Voltage		$V_{CC}$	50	V	
Input Voltage		$V_{IN}$	-6 ~ +40	V	
Outrat Outrat		I <sub>OUT</sub>	70	mA	
Output Current		I <sub>O(MAX.)</sub>	70 r x.) 100 r 200	mA	
	SOT-23/SOT-323		200	mW	
Davis Diagination	SOT-523	Б	150		
Power Dissipation	TO-92	$P_D$	625		
	TO-92SP		550		
Junction Temperature		TJ	+150	°C	
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## ■ **ELECTRICAL SPECIFICATIONS** (T<sub>A</sub>=25°C, unless others specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V <sub>IN(OFF)</sub>	V <sub>CC</sub> =5V, I <sub>OUT</sub> =100μA			0.3	V
	$V_{IN(ON)}$	V <sub>OUT</sub> =0.3V, I <sub>OUT</sub> =1mA	1.4			V
Output Voltage	V <sub>OUT(ON)</sub>	$I_{OUT}/I_{IN} = 5mA/0.25mA$		0.1	0.3	V
Input Current	I <sub>IN</sub>	V <sub>IN</sub> =5V			0.88	mA
Output Current	I <sub>OUT(OFF)</sub>	V <sub>CC</sub> =50V, V <sub>IN</sub> =0V			0.5	μΑ
DC Current Gain	h <sub>FE</sub>	V <sub>OUT</sub> =5V, I <sub>OUT</sub> =5mA	68			
Input Resistance	R <sub>1</sub>		7	10	13	ΚΩ
Resistor Ratio	$\frac{R_2}{R_1}$		3.7	4.7	5.7	
Transition Frequency	f <sub>T</sub>	$V_{CE}$ =10V, $I_E$ =-5mA, f=100MHz		250	·	MHz

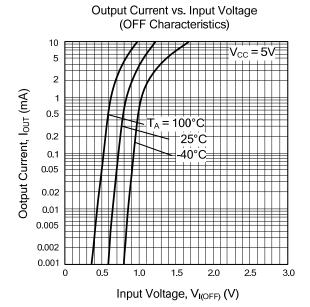
Note: Transition frequency of the device

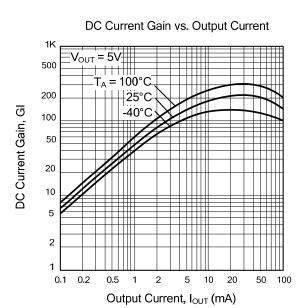
#### ■ TYPICAL CHARACTERISTICS

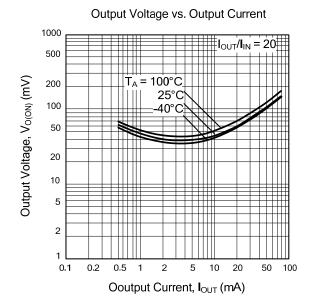
Input Voltage vs. Output Current

(ON Characteristics) 100  $V_{\text{OUT}} = 0.3V$ 50 20 Input Voltage, V<sub>I(ON)</sub> (mV) 10 5 25°C 100°C 2 500m 200m 100m 0.1 0.2 0.5 20 50 100

Output Current, I<sub>OUT</sub> (mA)







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