UNISONIC TECHNOLOGIES CO., LTD

DTC115T

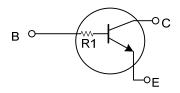
NPN SILICON TRANSISTOR

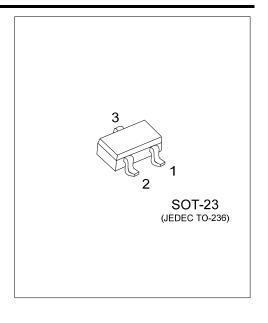
NPN DIGITAL TRANSISTOR (BUILT- IN BIAS RESISTORS)

■ FEATURES

- * Built-in bias resistors that implies easy ON/OFF applications.
- * The bias resistors are thin-film resistors with complete isolation to allow negative input.

■ EQUIVALENT CIRCUIT

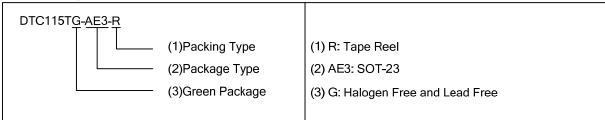




ORDERING INFORMATION

Order Number	Package	Pin Assignment			Dooking	
		1	2	3	Packing	
DTC115TG-AE3-R	SOT-23	E	В	С	Tape Reel	

Note: Pin Assignment: E: Emitter B: Base C: Collector



■ MARKING



■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless others specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	$V_{\sf CEO}$	50	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	Ιc	100	mA
Collector Power dissipation	Pc	200	mW
Junction temperature	T_J	150	°C
Storage temperature	T _{STG}	-55 ~ +150	°C

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **ELECTRICAL SPECIFICATIONS** (T_A=25°C, unless others specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV_CBO	I _C =50μA	50			V
Collector-Emitter Breakdown Voltage	BV_CEO	I _C =1mA	50			V
Emitter-Base Breakdown Voltage	BV_{EBO}	I _E =50μA	5			V
Collector Cutoff Current	I _{CBO}	V _{CB} =50V			0.5	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V			0.5	μA
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	I _C =1mA, I _B =0.1mA			0.3	V
DC Current transfer Ratio	h_{FE}	V _{CE} =5V, I _C =1mA	100	250	600	
Input Resistance	R1		70	100	130	ΚΩ
Transition Frequency	f_{T}	V _{CE} =10V, I _E =-5mA, f=100MHz		250		MHz

Note: Transition frequency of the device

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