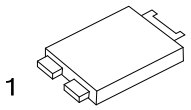
MGBR10L150

Preliminary ***DIODE***

MOS GATED BARRIER RECTIFIER



TO-277

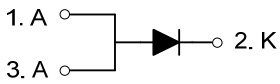
■ DESCRIPTION

The UTC **MGBR10L150** is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

* Low forward voltage drop
* High switching speed

■ SYMBOL



■ ORDERING INFORMATION

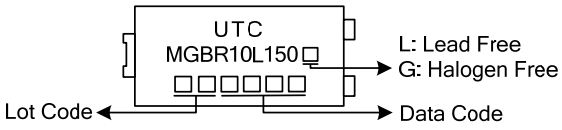
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
MGBR10L150L-T27-R	MGBR10L150G-T27-R	TO-277	A	K	A	Tape Reel

Note: Pin Assignment: A: Anode K: Common Cathode

MGBR10L150L-T27-R

(1)Packing Type	(1) R: Tape Reel
(2)Package Type	(2) T27: TO-227
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	150	V
Working Peak Reverse Voltage		V_{RWM}	150	V
Peak Repetitive Reverse Voltage		V_{RRM}	150	V
Average Rectified Output Current	T_C=125°C	I_O	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I_{FSM}	150	A
Operating Junction Temperature		T_J	-65~+175	°C
Storage Temperature		T_{STG}	-65~+175	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	73	°C/W
Junction to Case	θ_{JC}	13	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A =25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I_R=0.50mA	150			V
Forward Voltage Drop	V_{FM}	I_F=10A, T_J=25°C		0.8	0.9	V
		I_F=10A, T_J=125°C			0.8	V
Leakage Current (Note 1)	I_{RM}	V_R=150V, T_J=25°C		25	200	μA
		V_R=150V, T_J=125°C			50	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.
2. Thermal resistance junction to case mounted on heatsink.