

MGBR2V45

Preliminary

MOS GATED BARRIER RECTIFIER

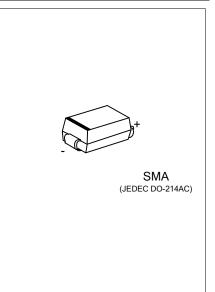
DESCRIPTION

The UTC **MGBR2V45** is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

* Very low forward voltage drop* High switching speed



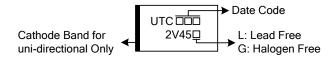


ORDERING INFORMATION

Ordering Number		Deekere	Pin Assignment		Decking	
Lead Free	Halogen Free	Package	1	2	Packing	
MGBR2V45L-SMA-R MGBR2V45G-SMA-R		SMA	К	А	Tape Reel	
Note: Pin Assignment: A: Anode K: Common Cathode						

MGBR2V45L- <u>SMA</u> -R (1)Packing Type	(1) R: Tape Reel
(2)Package Type	(2) SMA: SMA
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.			
PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V _{RM}	45	V
Working Peak Reverse Voltage	V _{RWM}	45	V
Repetitive Peak Reverse Voltage	V _{RRM}	45	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Rectified Output Current T _C =	140°C l _o	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3 Single Half Sine-Wave Superimposed on Rated	5014	50	А
Operating Junction Temperature	TJ	-65~+175	°C
Storage Temperature	T _{STG}	-65~+175	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL RESISTANCES CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ _{JA}	90	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_A =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.5mA	45			V
Instantaneous Forward Voltage	V_{FM}	I _F =2A, T _J =25°C			0.50	V
		I _F =2A, T _J =125°C			0.47	V
Lookago Current (Note 1)	I _{RM}	V _R =45V, T _J =25°C			100	μA
Leakage Current (Note 1)		V _R =45V, T _J =125°C			10	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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