

UNISONIC TECHNOLOGIES CO., LTD

MGBR5S40 Preliminary DIODE

MOS GATED BARRIER RECTIFIER

■ DESCRIPTION

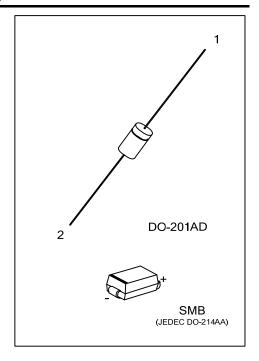
The UTC MGBR5S40 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Super low forward voltage drop
- * High switching speed

■ SYMBOL

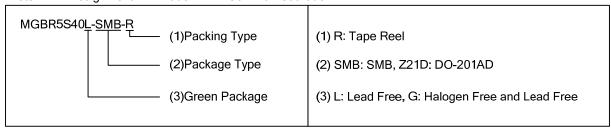




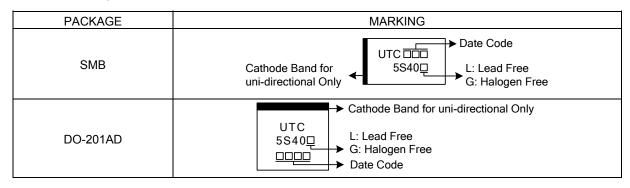
■ ORDERING INFORMATION

Ordering Number		Doolsono	Pin Assignment		Dooking	
Lead Free	Halogen Free	Package	1	2	Packing	
MGBR5S40L-SMB-R	MGBR5S40G-SMB-R	SMB	K	Α	Tape Reel	
MGBR5S40L-Z21D-R	MGBR5S40G-Z21D-R	DO-201AD	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V_{RM}	40	V
Working Peak Reverse Voltage	V_{RWM}	40	V
Peak Repetitive Reverse Voltage	V_{RRM}	40	٧
Average Rectified Output Current T _C =140°C	Ιο	5	Α
Non-Repetitive Peak Forward Surge Current 8.3ms		150	Α
Single Half Sine-Wave Superimposed on Rated Load	IFSM	150	
Operating Junction Temperature	T_J	-65~+150	°C
Storage Temperature	T_{STG}	-65~+150	Ŝ

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	_	SYMBOL	RATINGS	UNIT	
Lunction to Ambient	SMB	0	70	°C/W	
Junction to Ambient	DO-201AD	θ_{JA}	38	°C/W	
lunction to Coop	SMB	0	35	°C/W	
Junction to Case	DO-201AD	θ_{JC}	15	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C, unless otherwise specified.)

PARAMETER SYMBOL TEST CONDITIONS		TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage	$V_{(BR)R}$	I _R =0.5mA	40			V
lastartara sua Farrand Valtara	V _{FM}	I _F =5A, T _J =25°C		0.46	0.52	V
Instantaneous Forward Voltage		I _F =5A, T _J =125°C		0.42	0.48	V
Lookage Current	I _{RM}	V _R =40V, T _J =25°C			500	μA
Leakage Current		V _R =40V. T _{.I} =125°C			50	mA

Note: Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

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