MGBR10L200 Preliminary

MOS GATED BARRIER RECTIFIER

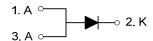
■ DESCRIPTION

The UTC MGBR10L200 is a surface mount mos gatedbarrier rectifier, it uses UTC's advanced technology to provide customers withlow forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed





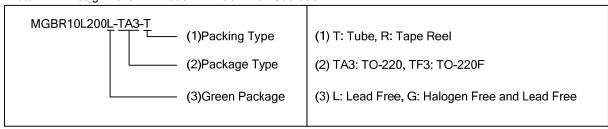
TO-220F

DIODE

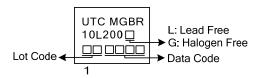
■ ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR10L200L-TA3-T	MGBR10L200G-TA3-T	TO-220	Α	K	Α	Tube	
MGBR10L200L-TF3-R	MGBR10L200G-TF3-R	TO-220F	Α	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V_{RM}	200	V
Working Peak Reverse Voltage	V_{RWM}	200	V
Repetitive Peak Reverse Voltage	V_{RRM}	200	٧
Average Rectified Output Current	lo	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	180	Α
Operating Junction Temperature	T_J	-65 ~ +150	°C
Storage Temperature	T_{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER		SYMBOL	RATINGS	UNIT
Typical Thermal Desistance	TO-220	0	2	°C/W
Typical Thermal Resistance	TO-220F	θ _{JC}	4	°C/W

■ **ELECTRICAL CHARACTERISTICS (PER LEG)** (T_A=25°C,unless otherwise specified.)

PARAMETER SYMBOL		TEST CONDITIONS		TYP	MAX	UNIT
Reverse Breakdown Voltage	$V_{(BR)R}$	I _R =0.5mA	200			V
Instantaneous Famuerd Voltage		I _F =10A, T _J =25°C			0.86	٧
Instantaneous Forward Voltage	V_{FM}	I _F =10A, T _J =125°C			0.75	V
Laskana Cumant		V _R =200V, T _J =25°C			100	μA
Leakage Current	IRM	V _R =200V, T _J =125°C			10	mA

Note: Pulse Test: Pulse width $\leq 300 \mu s$, Duty cycle $\leq 2\%$.

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